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23494 7590 02/03/2005

TEXAS INSTRUMENTS INCORPORATED
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02/25/2005 SDIRETAP 00000052 200668 09943137

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Robin E. Barnum	(Depositor's name)
	(Signature)
February 24, 2005	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/943,137	08/30/2001	Gary L. Swoboda	T1-30483	9656

TITLE OF INVENTION: USING SELECTIVE OMISSION TO COMPRESS ON-CHIP DATA PROCESSOR TRACE AND TIMING INFORMATION FOR EXPORT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	05/03/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
WILSON, YOLANDA L	2113	714-045000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list

(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 Robert D. Marshall, Jr.

2 W. James Brady, III

3 Frederick J. Telecky, Jr.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Texas Instruments Incorporated

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Dallas, TX USA

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

4a. The following fee(s) are enclosed:

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a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Date February 24, 2005

Typed or printed name

Robert D. Marshall, Jr.

Registration No. 28,527

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From

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-30483

Gary L. Swoboda

Art Unit: 2113

Serial No.: 09/943,137

Examiner: Yolanda L. Wilson

Filed: August 30, 2001

Conf. No.: 9656

For: Using Selective Omission to Compress On-Chip Data Processor Trace and Timing

Information for Export

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-746-4000 on the date shown below:

Robin E. Barnum

February 24, 2005
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NAME OF INVENTOR(S):		RECEIPT DATE & SERIAL NO.:
Gary L. Swoboda		Serial No.: 09/943,137
TITLE OF INVENTION: Using Selective Omission to Compress On-Chip Data Processor Trace and Timing Information for Export		Filing Date: August 30, 2001
TI FILE NO.:	DEPOSIT ACCT. NO.:	
TI-30483	20-0668	
FAXED: 2/24/05	DUE: 5/3/05	
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